



schedulers context switch

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J Zahorjan, C McCann - 1989 - portal.acm.org

... Because dynamic **schedulers** tend to shift processors mom frequently from one job to another, and because this "**context switch**" can be quite expensive, the ...Cited by 123 - [Web Search](#) - [Library Search](#)**Multiprocessor on-line scheduling of hard-real-time tasks - group of 9 »**

ML Dertouzos, A Mok - IEEE Transactions on Software Engineering, 1989 - doi.ieeecs.org

... processors involved, it is impractical to design optimal run-time **schedulers** if there ... Lemma I: The system overhead due to **context switch**- ing required by the ...Cited by 152 - [Web Search](#)**[PS] Threads and Input/Output in the Synthesis Kernel - group of 12 »**

H Massalin, C Pu - ACM SIGOPS Operating Systems Review, 1989 - guir.cs.berkeley.edu

... **Schedulers** use gauges to collect data for scheduling decisions. ... In two instances we can optimize **context switch** by moving data only when they are used. ...Cited by 107 - [View as HTML](#) - [Web Search](#)**Visualizing performance debugging - group of 5 »**

T Lehr, Z Segall, DF Vrsalovic, E Caplan, AL Chung ... - Computer, 1989 - doi.ieeecs.org

... dramatically reduce the number of **context**-switches because they permit threads to **switch** to themselves ... To the designer of these **schedulers** (David Black ...Cited by 56 - [Web Search](#) - [Library Search](#)**Scheduling support for concurrency and parallelism in the Mach operating system - group of 15 »**

DL Black - IEEE Computer, 1990 - ieeexplore.ieee.org

... Another impact on **schedulers** comes from the increased use of concurrency ... This rescheduling check causes a **context switch** if there is another runnable thread of ...Cited by 98 - [Web Search](#) - [Library Search](#)**CHIMERA II- A real-time multiprocessing environment for sensor-based robot control - group of 5 »**

D STEWART, D SCHMITZ, P KHOSLA - IEEE, International Symposium on Intelligent Control, Albany ..., 1989 - ieeexplore.ieee.org

... special purpose CPUS and I/O devices, a real-time multitasking kernel; user definable and dynamically selectable real-time **schedulers**; transparent access to ...Cited by 10 - [Web Search](#)**Transient Overloads in Fault-Tolerant Real-Time Systems - group of 2 »**

PM Thambidurai, KS Trivedi - IEEE Real-Time Systems Symposium, 1989 - ieeexplore.ieee.org

... of a dedicated "hardware distri- bution net." The total **context switch** time is ... The MAFT approach guarantees that the **schedulers** of all non-faulty nodes in ...Cited by 13 - [Web Search](#)**[PS] Fine-Grain Adaptive Scheduling using Feedback - group of 2 »**

H Massalin, C Pu - Computing Systems, 1989 - cse.ogi.edu

... real I/O from cheating I/O. This kind of cheating would succeed under existing

schedulers. ... magnitude of interrupt and **context switch** overhead. ...

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[\(PS\) Mach Kernel Monitor \(with applications using the PIE environment\) - group of 7 »](#)

T Lehr, D Black - Available on host mach. cs. cmu. edu in/usr/mach/public/doc/ ..., 1990 - nice.ch

... The **schedulers** of the latter two kernels use a ... to dramatically reduce the number of **context-switches** because it permits threads to **switch** to themselves ...

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[A VLIW architecture for a trace scheduling compiler - group of 11 »](#)

RP Colwell, RP Nix, JJ O'Donnell, DB Papworth, PK ... - IEEE Transactions on Computers, 1988 - doi.ieeecs.org

... In order to establish the **context** for such claims, we present Table I. Linpack numbers are from Dongarra's report [121; ANSYS numbers are from Swanson ...

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